IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s):

Luca G. Fasoli et al.

Assignee:

SanDisk 3D LLC

Title:

MEMORY ARRAY INCORPORATING MEMORY CELLS ARRANGED

IN NAND STRINGS

Patent No.:

7,221,588

Issued:

May 22, 2007

Atty. Docket No.: 023-0031

July 10, 2007

ATTN: Certificate of Correction Branch COMMISSIONER FOR PATENTS P.O. Box 1450 Alexandria, VA 22313-1450

REQUEST FOR CERTIFICATE OF CORRECTION OF PATENT – PTO MISTAKE (37 C.F.R. § 1.322(a))

Dear Sir:

Pursuant to 35 U.S.C. § 254 and 37 C.F.R. § 1.322(a), please issue a Certificate of Correction in the above-identified matter. The mistake(s) to be corrected was made by the Office.

- 1. Attached hereto is Form PTO/SB/44 (PTO-1050) suitable for printing.
- 2. The exact page(s) and line number(s) where the error(s) is shown correctly in the application file:
- 3. Application filed December 5, 2003, page 49, Abstract;

Response after Final Rejection filed January 3, 2007, page 2, claim 39 - claim 5 as issued was then pending claim 39.

4. Please send the Certificate to:

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Respectfully submitted

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UNITED STATES PATENT AND TRADEMARK OFFICE

CERTIFICATE OF CORRECTION

PATENT NO : 7,221,588
APPLICATION NO.: 10/729,843
ISSUE DATE : May 22, 2007

INVENTOR(S): Luca G. Fasoli, et al.

It is certified that error(s) appear(s) in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

On the Cover Sheet, in Section (57) Abstract: line 5, the word "and" should read -- said --.

Column 30:

Claim 5, line 6, the word "NAINI)" should read -- NAND --.

MAILING ADDRESS OF SENDER (Please do not use customer number below): ZAGORIN O'BRIEN GRAHAM LLP 7600B N. Capital of Texas Hwy., Suite 350 Austin, Texas 78731